



PTO/SB/08A (08-03)

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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/715,054
Filing Date	11/17/2003
First Named Inventor	James Y. Cho
Art Unit	2181
Examiner Name	
Attorney Docket Number	BP1745CON

Sheet 1 of 3

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
bh		US- 2002/0147875	10-2002	Singh et al.	
wh		US- 6,378,017	04-2002	Girzon et al.	
wh		US- 6,442,642	08-2002	Brooks, John Milford	
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wh		US- 6,078,546	06/2000	Lee	
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FOREIGN PATENT DOCUMENTS

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		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				

Examiner
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Kim T. Huh

Date
Considered

6/12/06

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		Examiner Name			
Sheet	2	of	3	Attorney Docket Number	BP1745CON

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
WA		"Power PC 601; RISC Microprocessor User's Manual," IBM Microelectronics, Power PC, Motorola, Rev.1, 1993, 8 pages.	
		SiByte, "Target Applications," http://sibyte.com/mercurian/applications.htm , January 15, 2001, 2 pages.	
		SiByte, "SiByte Technology," http://sibyte.com/mercurian/technology.htm , January 15, 2001, 3 pages.	
		SiByte, "The Mercurian Processor," http://sibyte.com/mercurian , January 15, 2001, 2 pages.	
		SiByte, "Fact Sheet," SB-1 CPU, October 2000, rev. 0.1, 1 page.	
		SiByte, "Fact Sheet," SB-1250, October 2000, rev. 0.2, 10 pages.	
		Stepanian, SiByte, "SiByte SB-1 MIPS64 CPU Core, Embedded Processor Forum 2000, June 13, 2000, 15 pages.	
		Jim Keller, "The Mercurian Processor: A High Performance, Power-Efficient CMP for Networking," October 10, 2000, 22 pages.	
		Halfhill, "SiByte Reveals 64-Bit Core for NPUs," Microprocessor Report, June 2000, pp. 45-48.	
WA		Pentium Pro Family Developer's Manual, Vol. 1: Specifications, Chapter 3, pp. 1-25, 1996.	

Examiner Signature	Wm T. Hynh	Date Considered	6/12/08
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WH		Tom R. Halfhill, "SiByte Reveals 64-Bit Core For NPU's; Independent MIPS64 Design Combines Low Power, High Performance," Microdesign Resources, 06/2000, Microprocessor Report, 4 pages	

Examiner Signature	Wm T. Wynn	Date Considered	6-12-06
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